IN THE CLAIMS

Please amend the claims as follows:

1-8. (Cancelled)

9. (Currently Amended) A dual stage current limiting surge protector system for protecting telecommunications equipment ~~and the like~~ from power and transient surges, comprising:

input tip and ring terminal pins;

output tip and ring terminal pins;

first voltage suppressor means having first and second ends operatively coupled between said input tip and ring terminal pins;

the first and second ends of said first voltage suppressor means being also operatively coupled between said output tip and ring terminal pins;

first and second fuse elements interconnected between said input tip and ring terminal pins and the

respective first and second ends of said first voltage suppressor means; [and]

third and fourth current fuse elements interconnected between said output tip and ring terminal pins and the respective first and second ends of said first voltage suppressor means[[.]];

each of said third and fourth fuse elements having a lower rated current value than each of said first and second fuse elements;

second voltage suppressor means having first and second ends operatively coupled between said output tip and ring terminal pins; and

said second voltage suppressor means having a predetermined breakdown voltage that is less that the breakdown voltage of said first voltage suppressor means.

10. (Cancelled)

11. (Original) A dual stage current limiting surge protector system as claimed in Claim 9, wherein each of said third and fourth fuse elements has a rated current value of about 175 ma and each of said first and second fuse elements has a rated current value of about 350 ma.

12. (Original) A dual stage current limiting surge protector system as claimed in Claim 9, wherein said voltage suppressor means is comprised of a silicon avalanche suppressor.

13. (Original) A dual stage current limiting surge protector system as claimed in Claim 9, wherein said voltage suppressor means is comprised of a sidactor.

14. (Original) A dual stage current limiting surge protector system as claimed in Claim 9, wherein said voltage suppressor means is comprised of a gas discharge tube.

15. (Cancelled)

16. (Currently Amended) A dual stage current limiting surge protector system for protecting telecommunications equipment ~~and the like~~ from power and transient surges, comprising:

input tip and ring terminal pins;

output tip and ring terminal pins;

first voltage suppressor means having first and second ends operatively coupled between said input tip and ring terminal pins;

the first and second ends of said first voltage suppressor means being also operatively coupled between said output tip and ring terminal pins;

first and second positive thermal coefficient resistors interconnected between said input tip and ring terminal pins and the respective first and second ends of said first voltage suppressor means; ~~and~~

third and fourth positive thermal coefficient resistors interconnected between said output tip and ring terminal pins and the respective first and second ends of said first voltage suppressor means[[.]];

each of said third and fourth positive thermal coefficient resistors having a lower rated current value than each of said first and second positive thermal coefficient resistors;

second voltage suppressor means having first and second ends operatively coupled between said output tip and ring terminal pins; and

said second voltage suppressor means having a predetermined breakdown voltage that is less that the breakdown voltage of said first voltage suppressor means.

17. (Cancelled)

18. (Original) A dual stage current limiting surge protector system as claimed in Claim 16, wherein each of said third and fourth positive thermal coefficient resistors has a rated current value of about 80 ma and each of said first and second positive thermal coefficient resistors has a rated current value of about 160 ma.

19. (Original) A dual stage current limiting surge protector system as claimed in Claim 16, wherein said voltage suppressor means is comprised of a silicon avalanche suppressor.

20. (Original) A dual stage current limiting surge protector system as claimed in Claim 16, wherein said voltage suppressor means is comprised of a sidactor.

21. (Original) A dual stage current limiting surge protector system as claimed in Claim 16, wherein said voltage suppressor means is comprised of a gas discharge tube.

22. (Cancelled)